

	WEEK#	Remy	Loic
JUNE	23	charger robustness (Boost ac analysis)	accuracy
	24	charger robustness	accuracy high duty cycle
	25	charger robustness ntc monitoring	accuracy high duty cycle
	26	Congé	high duty cycle cboot refresh digital
JULY	27	Congé	dvs new settings
	28	Congé	efficiency optimization
	29	misc topcell	
	30		Congé
AUGUST	31		
	32		
	33		
	34		Congé
	35		Congé
	36		

Laurent	Franck	Geraldine
Congé	PMIC	esd on CAP pin
Congé	PMIC	HBM issue
boost 560mA + 5.1V Boost ac analysis	Digital Design	esd with nldmos 24V
Rbfet 24V boost stab issue at low temp	Digital Design	
OVP issue	Digital Design	
leoc programmation	Congé	
jeita chilly threshold 100k Rpu	Congé	
	Digital Design backend	
Congé		

Paolo	Aurore	Patrice
bandgap analysis vcore deregulation	hss driver sub to split from neighbour block pad ring vin_monitor_top	gp ota
	Rbfet + hss iin current meter	regulator 2.4V shifted ground core references vbg
	dvs	ntc monitoring
	error amp	resistor ladder for 5.1V
Congé	ieoc prog	ipk for 2A
	boost precharge resizing	
	OTP 64 bits ?	
	esd	100k ntc rpu
		jeita
	checking	checking
	checking	checking