

Voltage Storm 2005 Design Kit Requirements

CELL BASED VSC/VSPE/VSDG	TRANSISTOR BASED VST	VAVO/VAEO
a.Design DEF or GDSIII (not OpenAccess) b.Power Consumption Estimation c.IP === Condor	IP === Condor	IP === Ethernet
1. Process.ict (R network)	Process.ict (R network)	
2. Technology LEF	Xtc.cmd	LVS rule deck
3. Macro LEF	tablegen	RCX rule deck
4. Spice models (BSIM3)		
? Process.ict (C network for powermeter)	Electromigration.em	
? Standardcell.cl (libgen)	Standardcell.cl (libgen)	
? Libgen.cmd	Memory.cl	
? Design VCD file (VSDG only)		

2005 Demo/Workshop CDK Requirements

- What does CIC Core Comp need for Voltage Storm 2005?
 - VSPE IR drop analysis on ethernet blocks
 - VAVO? VAEO?
 - Anything else?
- What do the digital guys need for Voltage Storm 2005?
 - Nothing?